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shared interrupts

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## Patents

Patents 1 - 10 on **shared interrupts**. (0.55 seconds)

### System for managing a plurality of **shared** interrupt handlers in a linked-list data structure

US Pat. 4768149 - Filed Aug 29, 1985 - International Business Machines Corporation

20 The resulting **shared** interrupt apparatus and method invention provides ...In this manner, the loss of **interrupts** is pre-vented if more than one of the ...

### Inter-thread communications using **shared** interrupt register

US Pat. 6971103 - Filed Apr 1, 2003 - Sandbridge Technologies, Inc.

... if the destination thread is enabled for receipt of cross-thread **interrupts**;... the cross-thread interrupt register being **shared** by a plurality of ...

### Apparatus and method for detecting and updating status of removable storage system using **shared** interrupt and software flag

US Pat. 5590376 - Filed Nov 13, 1992 - Toshiba America Information Systems, Inc.

... control signals such as **interrupts** trans-mitted between components of a ...Consequently, the use of this interrupt is "**shared**" between indicating that ...

### Method and apparatus for interrupt signaling in a computer system

US Pat. 5535420 - Filed Dec 14, 1994 - Intel Corporation

A global router is implemented for mapping **interrupts** received over a mul-tiple-line**shared** interrupt bus to correspond to system standard IRQ interrupt ...

### Method and apparatus for detecting interrupt requests in video graphics and other systems

US Pat. 6279067 - Filed Jan 13, 1999 - ATI International SRL

At step 202, it is determined if the **shared** interrupt 2s request flag is set.... and method for polling described herein allows **interrupts** to be detected ...

### Computer system generating a processor interrupt in response to receiving an interrupt/data synchronizing signal over a data bus

US Pat. 6038629 - Filed Sep 17, 1998 - International Business Machines Corporation

Two **interrupts** are allo-cated for external use. One interrupt services a **shared**

interrupt known as the Asynchronous Interrupt Mechanism (AIM), ...

### Apparatus for processing PCMCIA interrupt requests

US Pat. 5475846 - Filed Aug 11, 1993 - Databook Incorporated

A deficiency in the PCMCIA specification has caused problems with the traditional operation of **shared interrupts**. PCMCIA defines a standard for ports and ...

### Self-nesting interrupts

US Pat. 7043582 - Filed Sep 6, 2002 - Intel Corporation

... said second interrupt have a **shared** interrupt request line of the same prioritylevel, and said first and second **interrupts** are received from said first ...

### Interrupt level sharing

US Pat. 4631670 - Filed Jul 11, 1984 - IBM Corporation

... is necessary in a compatible system where the interrupt mask register of the previously written soft-ware would disable all the **shared interrupts**. ...